

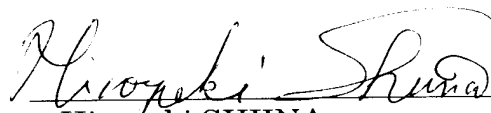
DECLARATION

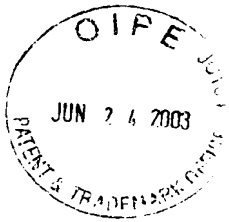
I, Hiroyuki SHIINA, a national of Japan, c/o Central Research Laboratory of Hitachi, Ltd. of 1-280, Higashikoigakubo, Kokubunji-shi, Tokyo, Japan, do hereby solemnly and sincerely declare:

- 1) THAT I am well acquainted with the Japanese language and English language, and
- 2) THAT the attached is a full, true and faithful translation into the English language made by me of Japanese Patent Application No. 09-320973 filed on November 21, 1997.

The undersigned declares further that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true ; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signed this 27th day of May, 2003


Hiroyuki SHIINA



【Document Name】 Application for Patent
【Reference No.】 H97022411
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【Destination】 Commissioner, Patent Office
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【Title of the Invention】 A semiconductor device
【No. of Claims】 18
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【Designation of Charge】

【Ledger No. for Prepayment】 014579

【Amount of Payment】 21000

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【Number of Comprehensive Power of Attorney】 9003109

【Proof】 Required